

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10779904			
<b>Filing Date:</b>	17-Feb-2004			
<b>Title of Invention:</b>	Method and apparatus for defect analysis of semiconductor integrated circuit			
<b>First Named Inventor/Applicant Name:</b>	Masahiro Ishida			
<b>Filer:</b>	Thomas K. Scherer/Sophie Bolt			
<b>Attorney Docket Number:</b>	02008.071003			
Filed as Large Entity				
<b>Utility under 35 USC 111(a) Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
Request for continued examination	1801	1	810	810
<b>Total in USD (\$)</b>				<b>810</b>